



Intel® Desktop Board DG35EC Specification Update

February 2009

Order Number: E39395-003US

The Intel® Desktop Board DG35EC may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

Revision History

Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board DG35EC	March 2008
-002	Updated General Information Section	October 2008
-003	Updated General Information Section	February 2009

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The Intel® Desktop Board DG35EC may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

Intel Corporation
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Specification Update for the Intel® Desktop Board DG35EC

For specification updates concerning the Intel® processor that may apply to this desktop board, refer to the following:

- Intel® Pentium® D Processor 900Δ Sequence and Intel® Pentium® Processor Extreme Edition 955Δ, 965Δ Specification Update (Order Number 310307)
- Intel® Pentium® D Processor 800Δ Sequence and Intel® Pentium® Processor Extreme Edition 840Δ Specification Update (Order Number 306832)
- Intel® Celeron® D Processors 300Δ Sequence Specification Update (Order Number 302354)
- Intel® Pentium® 4 Processor Specification Update (Order Number 249199)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® G35 Chipset Family Specification Update* (Order Number 317608) for specification updates concerning the 82G35 MCH Controller and that may apply to the desktop board DG35EC. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® I/O Controller Hub 8 (ICH8) Family EDS Specification Update* (Order Number 313057) for specification updates concerning the 82801HB I/O Controller Hub and that may apply to the desktop board DG35C. Unless otherwise noted in this document, it should be assumed that any ICH8 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® Gigabit LAN Controller Family Specification Update* (Order Number 315551) for specification updates concerning the 82566DC LAN Controller and that may apply to the desktop board DG35C. Unless otherwise noted in this document, it should be assumed that any errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Terminology

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

General Information

Basic Desktop Board DG35EC Identification Information

AA Revision	BIOS Revision	Notes
E29266-202	ECG3510M.86A.0063	1-2
E29266-203	ECG3510M.86A.0072	1-2
E29266-204	ECG3510M.86A.0072	1-2
E29266-205	ECG3510M.86A.0084	1-2
E29266-206	ECG3510M.86A.0106	1-2
E29266-207	ECG3510M.86A.0106	1-2
E37012-202	ECG3510M.86A.0063	1-2
E37012-203	ECG3510M.86A.0072	1-2
E37012-204	ECG3510M.86A.0072	1-2
E37012-205	ECG3510M.86A.0084	1-2
E37012-206	ECG3510M.86A.0106	1-2
E37012-207	ECG3510M.86A.0106	1-2
E39017-202	ECG3510M.86A.0063	1-2
E39017-203	ECG3510M.86A.0072	1-2
E39017-204	ECG3510M.86A.0072	1-2
E39017-205	ECG3510M.86A.0084	1-2
E39017-206	ECG3510M.86A.0106	1-2
E39017-207	ECG3510M.86A.0106	1-2

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The 82G35 Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82G35 GMCH	E0	SLAJJ
82801HB ICH8	B0	SL9MN
82566DC	B1	SL97K

Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DG35EC. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

Codes Used in Summary Table

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
		There is no characterized erratum for this product.